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San Diego, CA 92127
(858) 675-1670 (voice)
(858) 675-1674 (fax)
e-mail: iprights@earthlink.net
www.gazpat.com

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VIA FACSIMILE (703-746-9195)

Office of Initial Patent Examination's
Filing Receipt Corrections
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: United States Divisional Patent Application for:
"EMBEDDED-DRAM-DSP ARCHITECTURE"
Inventor: Eric M. Dowling
Filed: February 13, 2002
Serial Number: 10/074,779
Atty. Docket: SEARCHP.011C1DV1

Dear Sir or Madam:

In connection with the above-referenced patent application, please correct our mailing address, telephone and facsimile numbers as follows:

Robert F. Gazdzinski
Gazdzinski & Associates
11440 West Bernardo Court, Suite 375
San Diego, CA 92127
(858) 675-1670 Telephone
(858) 675-1674 Facsimile

We submitted a Change of Address to the USPTO Document Dissemination Center for this application in December of 2002. See attached copy of our request. Please issue a Corrected Filing Receipt for this application, and return it to our office by United States mail or facsimile.

Thank you for your assistance in this request. If you have any questions, please feel free to call.

Sincerely,

GAZDZINSKI & ASSOCIATES


Robert F. Gazdzinski

RFG/cd